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8022278 ZV68

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1. Physical layer

Note: The IO-Link Device's max. current consumption (inclusive load current) shall not exceed the the master port's max. output power current.

SIO Modus	yes
Min Cycle Time	4.3 ms
Baudrate ²	COM2
Process Data Length	2 Byte
IODD version	V1.0
Valid for IO-Link version	1.1.0

2. Process data

PD StandardMode
Record : 2 Byte - Standard Mode

Bitoffset	15	14	13	12	11	10	9	8
Byte 0	Q1 Quality of Print							5
Type/Subindex	Unsigned Integer 8							3
Bitoffset	7	6	5	4	3	2	1	0
Byte 1						Q1 Valid		Q1
Type/Subindex						Boolean		Boolean

3. Service data

The following ISDUs will not be saved via Data-Storage: Direct Parameters 1, Direct Parameters 2, Teach-in channel, Device Specific Tag, Sender Off, Find me, Job Assurance Part 1, Job Assurance Part 2, Job Assurance Part 3, Job Assurance Part 4 and Notification handling

IO-Link specific							
Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
12 (0x0C)	Device Access Locks	Record ³	2 Byte	rw			
1 (0x01)	Parameter (write) Access Lock	Bit (0)	1 Bit	rw			Not relevant
2 (0x02)	Data Storage Lock	Bit (1)	1 Bit	rw			Disables Data Storage mechanism
3 (0x03)	Local Parameterization Lock	Bit (2)	1 Bit	rw			Locks user interface buttons (unlock only via IO-Link)
4 (0x04)	Local User Interface Lock	Bit (3)	1 Bit	rw			Locks user interface buttons (unlock via user interface buttons or IO-Link)
16 (0x10)	Vendor Name	String	7 Byte	ro	SICK AG		
17 (0x11)	Vendor Text	String	64 Byte	ro	www.sick.com		
18 (0x12)	Product Name	String	30 Byte	ro			
19 (0x13)	Product ID	String	13 Byte	ro			
20 (0x14)	Product Text	String	64 Byte	ro	PSS		
21 (0x15)	Serial Number	String	8 Byte	ro			
22 (0x16)	Hardware Version	String	4 Byte	ro			
23 (0x17)	Firmware Version	String	4 Byte	ro			
24 (0x18)	Application Specific Tag	String	32 Byte	rw			
36 (0x24)	Device Status	UInt	8 Bit	ro		0 = Device is OK 1 = Maintenance required 2 = Out of specification 3 = Functional check 4 = Failure 5...255 = Reserved	
40 (0x28)	Process Data Input	PD In	2 Byte	ro			

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² COM values specify the bitrate (see IO-Link specification): COM1 (4,8 kbit/s), COM2 (38,4 kbit/s), COM3 (230,4 kbit/s)

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SICK device specific							
Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
58 (0x3A)	Teach-in channel	UInt	8 Bit	rw		0 = default (BDC1 = Q1)	Only 1 Teach-channel available
59 (0x3B)	Teach-in status	UInt	8 Bit	ro	240	4 = Device is waiting for Background Trigger Window 5 = Device is recording Background 7 = Teach error 52 = Device is waiting for Reference Pattern Window 53 = Device is recording Reference Pattern 240 = Device is in Run Mode	
60 (0x3C)	Q1 SP1/SP2	Record	2 Byte	rw			
1 (0x01)	Setpoint SP1	Bit (8)	8 Bit	rw	50	10...90	Sensitivity level for detection output on Q1 in percent
2 (0x02)	Setpoint SP2	Bit (0)	8 Bit	rw	0	0 = Vendor Specific Default	Not relevant
61 (0x3D)	Q1 configuration	Record	4 Byte	rw			
1 (0x01)	Switchpoint logic	Bit (24)	8 Bit	rw	0	0 = Q1 active on match 1 = Q1 active on mismatch	
2 (0x02)	Switchpoint mode	Bit (16)	8 Bit	rw	1	1 = single point mode	Not relevant
3 (0x03)	Switchpoint hysteresis	Bit (0)	16 Bit	rw	0	0 = Vendor Specific Default	Not relevant
64 (0x40)	Device Specific Tag	String	32 Byte	rw			
82 (0x52)	Reference Pattern - Part 1	Array ³	232 Byte	rw		Unsigned Integer32 [58]	ISDUs 82 to 85 are for visualisation purposes only. Use Job Assurance or Data Storage mechanism to save and load jobs.
83 (0x53)	Reference Pattern - Part 2	Array ³	232 Byte	rw		Unsigned Integer32 [58]	
84 (0x54)	Reference Pattern - Part 3	Array ³	232 Byte	rw		Unsigned Integer32 [58]	
85 (0x55)	Background Pattern and Teach Meta Data	Array ³	232 Byte	rw		Unsigned Integer32 [58]	
97 (0x61)	Sender Off	UInt	8 Bit	rw	0	0 = Sender active 1 = Sender not active	No measurement with inactive sender possible
114 (0x72)	Teach-in Quality	UInt	8 Bit	ro		0...100	Quality level
117 (0x75)	Display Direction Switch	UInt	8 Bit	rw	0	0 = Reading direction not turned 1 = Reading direction upside-down	
121 (0x79)	Pin 2 Configuration	UInt	8 Bit	rw	17	0 = deactivated 17 = External teach 32 = Switching Output Q	
153 (0x99)	Temperature	Int	16 Bit	ro			Internal device temperature in °C
161 (0xA1)	Input Timer Mode	UInt	8 Bit	rw	0	0 = deactivated 1 = Ton delay 2 = Toff delay 3 = Ton + Toff delay 4 = Ton delay + impulse	Delay mode applied on trigger input pin
162 (0xA2)	Ton Input Delay	UInt	16 Bit	rw	1	1...30000	Delay time in milliseconds applied on rising edge of trigger input pin
163 (0xA3)	Toff Input Delay / Impulse Length	UInt	16 Bit	rw	1	1...30000	Delay time in milliseconds applied on falling edge of trigger input pin in input timer modes 2 and 3 and impulse length applied in mode 4
164 (0xA4)	Background Teach Evaluation	UInt	8 Bit	rw		0 = Background Teach disabled 1 = Background Teach enabled	If disabled any active background teach will be ignored. In this case no background can be thought during teach-in procedure, too.
165 (0xA5)	Last Print Pattern - Part 1	Array ³	232 Byte	ro		Unsigned Integer32 [58]	Read ISDUs 165 to 167 in ascending order (165 before 166 and 167) as pattern read from ISDUs 166 and 167 always matches last pattern read from ISDU 165
166 (0xA6)	Last Print Pattern - Part 2	Array ³	232 Byte	ro		Unsigned Integer32 [58]	
167 (0xA7)	Last Print Pattern - Part 3	Array ³	232 Byte	ro		Unsigned Integer32 [58]	
204 (0xCC)	Find me	UInt	8 Bit	rw	0	0 = deactivated 1 = yellow LED blink with 1 Hz 16 = yellow LED + Q (Pin 2) blink with 1 Hz	Only for identification purposes

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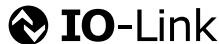
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SICK device specific

Index dec (hex)	Name	Format (Offset)	Length	Access ¹	Default Value	Value / Range	Remark [Unit]
219 (0xDB)	ProductID	Record ³	7 Byte	ro			
¹ (0x01)	Product ID Device	Bit (0)	7 Byte	ro			
222 (0xDE)	Job Assurance Part 1	Array ³	232 Byte	rw		Unsigned Integer8 [232]	One complete PSS job consists of all four Job Assurance IS-DUs (222 to 225). Use system command 208 to activate a loaded job after it has been written to ISDUs 222 to 225. Use system command 209 before reading out a job on the same ISDUs.
223 (0xDF)	Job Assurance Part 2	Array ³	232 Byte	rw		Unsigned Integer8 [232]	
224 (0xE0)	Job Assurance Part 3	Array ³	232 Byte	rw		Unsigned Integer8 [232]	
225 (0xE1)	Job Assurance Part 4	Array ³	232 Byte	rw		Unsigned Integer8 [232]	
227 (0xE3)	Notification handling	UInt	8 Bit	rw	0	0 = All enabled 1 = All disabled	Enable / Disable generation of IO-Link events
1085 (0x43 D)	Timer Mode Q1	UInt	8 Bit	rw	4	0 = deactivated 1 = Ton delay 2 = Toff delay 3 = Ton + Toff delay 4 = impulse	
1087 (0x43 F)	Time Setup Q1	UInt	16 Bit	rw	10	1...30000	Delay time applied on Q1 output in milliseconds
1089 (0x44 1)	Inverter Q1	UInt	8 Bit	rw	0	0 = not inverted 1 = inverted	

Standard command

Index dec (hex)		Access ¹	Value	Name	Remark [Unit]
2 (0x02)	Standard Command	wo	71	Start Teach (Background + Print)	Starts only the Teach-in procedure. One trigger window for background and one trigger window for print pattern needed.
			73	Start Teach (Print only)	Starts only the Teach-in procedure. One trigger window for print pattern needed.
			79	Abort Teach-In Sequence	
			128	Device Reset	
			130	Restore Factory Settings	
			208	Execute Job Assurance (Restore)	Write this command after a job has been written to ISDUs 222 to 225 to activate it.
			209	Show Present Job (Read Job)	Write this command before reading out a job from ISDUs 222 to 225.
			226	Trigger window start	Used to stimulate a pattern window start during Teach-in or run.
			227	Trigger window stop	Used to stimulate a pattern window stop during Teach-in or run.

Events

Code dec (hex)	Name	Type	Remark [Unit]
6144 (0x180 0)	Teach-in failure	Notification	
6145 (0x180 1)	Teach-in Background successful	Notification	
6146 (0x180 2)	Teach-in Print successful	Notification	
6147 (0x180 3)	Hardware Error	Error	Replace sensor

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Events			
Code dec (hex)	Name	Type	Remark [Unit]
16912 (0x4210)	Device temperature over-run	Warning	Clear source of heat
36000 (0x8CA0)	Output is shortcircuited	Warning	Check wiring of sensor

4. Job Assurance

Job assurance allows to set, save and manage parameters for specific formats or recipes via IO-Link. The following overview shows the necessary ISDUs.

1. Job

Q1 SP1/SP2 (60), Q1 configuration (61), Reference Pattern - Part 1 (82), Reference Pattern - Part 2 (83), Reference Pattern - Part 3 (84), Background Pattern and Teach Meta Data (85), Input Timer Mode (161), Ton Input Delay (162), Toff Input Delay / Impulse Length (163)

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